WE CLAIM

1. (Previously amended) A system for compensating for timing violations of a multiplex of at least two media packet streams, the system comprises:

a transmitter, operable to receive the multiplex, to associate transmitter timing information to media packets that belong to the multiplex, and to transmit the media packets and the associated transmitter timing information towards a receiver, over a timing violation inducing communication channel; wherein the transmitter comprises a transmitter time base generator that is adapted to generate the transmitter timing information without synchronizing to any to time bases associated with the media packet streams; whereas the at least two media packet streams are associated with multiple time; and

a receiver, operable to receive the transmitter timing information and the media packets, and to provide at least one timing violation compensated media packet stream in response to the transmitter timing information.

2. (Original) The system according to claim 1 further comprises a decoder for decoding the at least one timing violation compensated media packet stream.

3. (Canceled).

4. (Currently amended) The system of claim 3 1 wherein the receiver comprises a phased lock loop (PLL) for reconstructing the transmitter time base.

5. (Canceled).

6. (Original) The system of claim 1 wherein the transmitter is operable to encapsulate said media packets and the transmitter timing information in a communication channel format packets.

7. (Original) The system of claim 6, wherein the communication channel format packets does not comprise RTP compliant headers.

8. (Original) The system of claim 6 wherein the transmitter is operable to include the transmitter timing information within a communication channel format packet header.

9. (Canceled)

10. (Original) The system of claim 1 wherein the receiver comprises: a receiving end communication interface, connected to a splicer, wherein said receiving end communication interface receives said communication channel format packets from the timing violation inducing communication channel, wherein said receiving end communication interface provides said communication channel format packets to said

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splicer; and wherein the splicer is capable of extracting the transmitter timestamp and to provide it to a receiver PLL and of extracting the media packets.

11. (Original) The system of claim 1 wherein at least one media stream packet IS MPEG compliant.

12. (Previously amended) A system for reducing jitter of a multiple program transport stream, the system comprises:

a transmitter, operable to receive the multiple program transport stream from a low jitter communication channel, to associate a transmitter timing information to the packets of the multiple program transport stream, and to transmit the packets of the multiple program transport stream and the associated transmitter timing information over a high jitter communication channel towards a receiver; wherein the transmitter comprises a transmitter time base generator that is capable of generating a transmitter time base without synchronizing to any of at least two different time bases of at least two media packet streams of the multiple program transport stream; and

a receiver, coupled to the transmitter over the high jitter communication channel, the receiver is operable to receive the transmitter timing information and the packets of the multiple program transport stream, and to provide at least one low jittered program in response to the transmitter timing information.

13. (Previously amended) The system according to claim 12 further comprising a decoder for decoding the at least one program.

14. (Canceled).

15. (Previously amended) The system of claim 12, wherein the receiver comprises a phased lock loop (PLL) for reconstructing the transmitter time base.

16. (Canceld).

17. (Original) The system of claim 12 wherein the receiver comprises: a receiving end communication interface that receives media packets from the high jittered communication channel and provides the media packets to a splicer; and wherein the splicer is capable of extracting transmitter timing information as well as transport stream packets from the media packets.

18. (Previously amended) A method for compensating for timing violations of a multiplex of at least two media packet streams, the method comprising the steps of:
(a) generating the transmitter timing information, wherein the step of generating does not involve synchronizing to a time base associated with any of the media packet streams; (b) receiving the multiplex; (c) associating transmitter timing information to

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media packets that belong to the multiplex; (d) transmitting the media packets and the associated transmitter timing information towards a receiver, over a timing violation inducing communication channel; whereas the at least two media packet streams are associated with different time bases; (e) receiving the transmitter timing information and the media packets; and (f) providing at least one timing violation compensated media packet stream in response to the transmitter timing information.

19. (Original) The method of claim 18 further comprising a step of decoding the at least one timing violation compensated media packet stream.

20. (Cancelled).

21. (Cancelled).

22. (Original) The method of claim 18 wherein the step of providing comprises a step of reconstructing a transmitter time base from the transmitter timing information.

23. (Original) The method of claim 18 further comprising a step of converting media packets of the multiplex to communication channel format packets; whereas the communication channel format packets comprise the transmitter timing information.

24. (Original) The method of claim 23 wherein the communication channel format packets do not comprise RTP compliant headers.

25. (Canceled)

26. (Original) The method of claim 18 wherein at least one media stream packet is MPEG compliant.

27. (Original) The method of claim 18 wherein the multiplex is received over a low jitter communication channel.

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